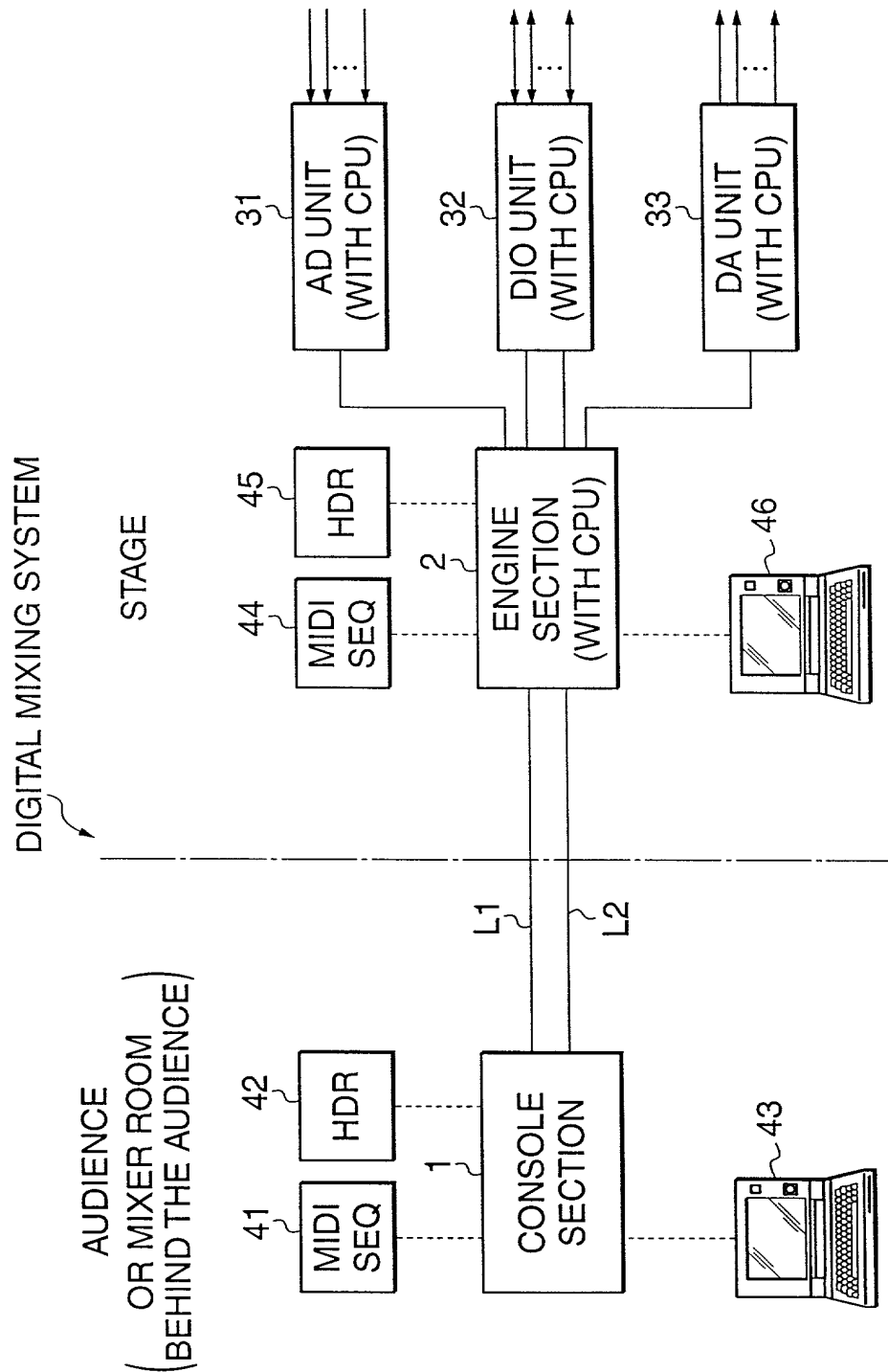


FIG. 1



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FIG. 2

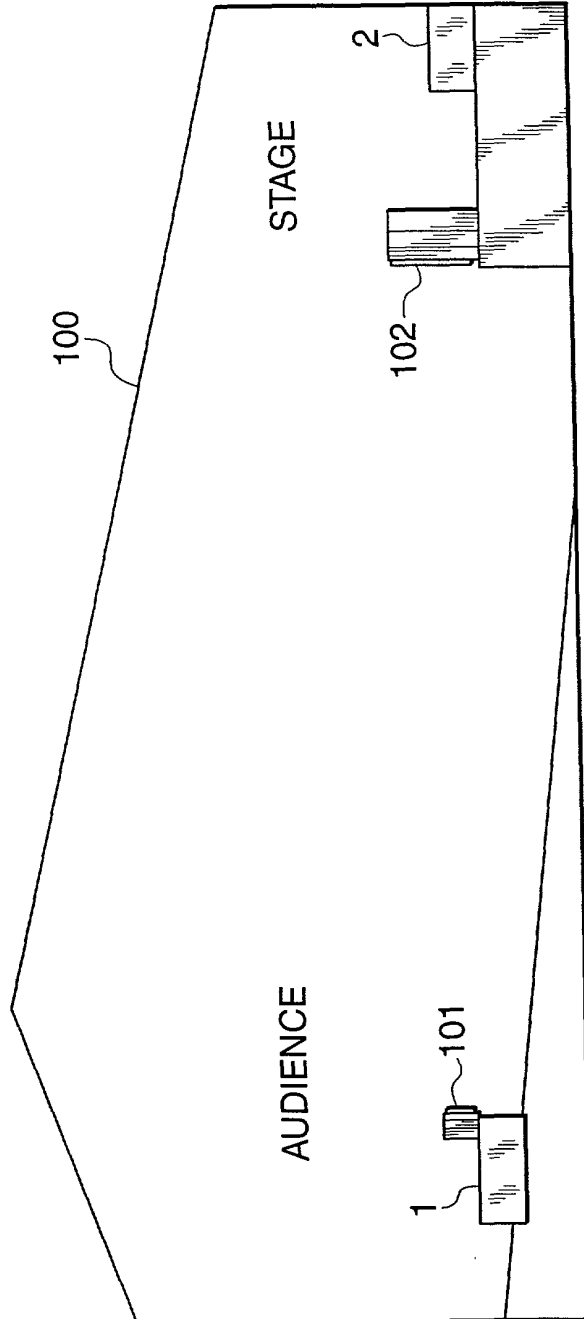


FIG. 3

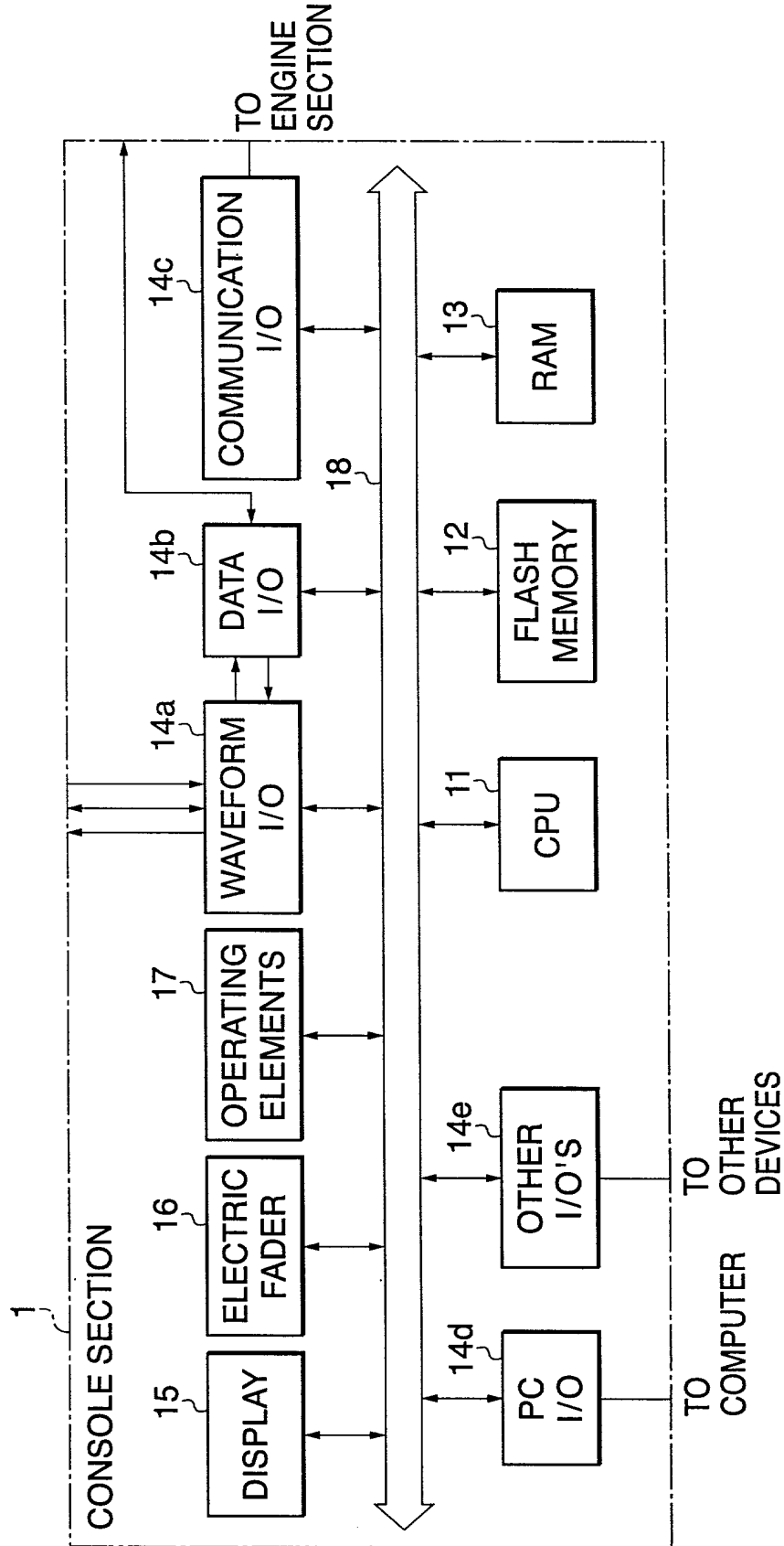
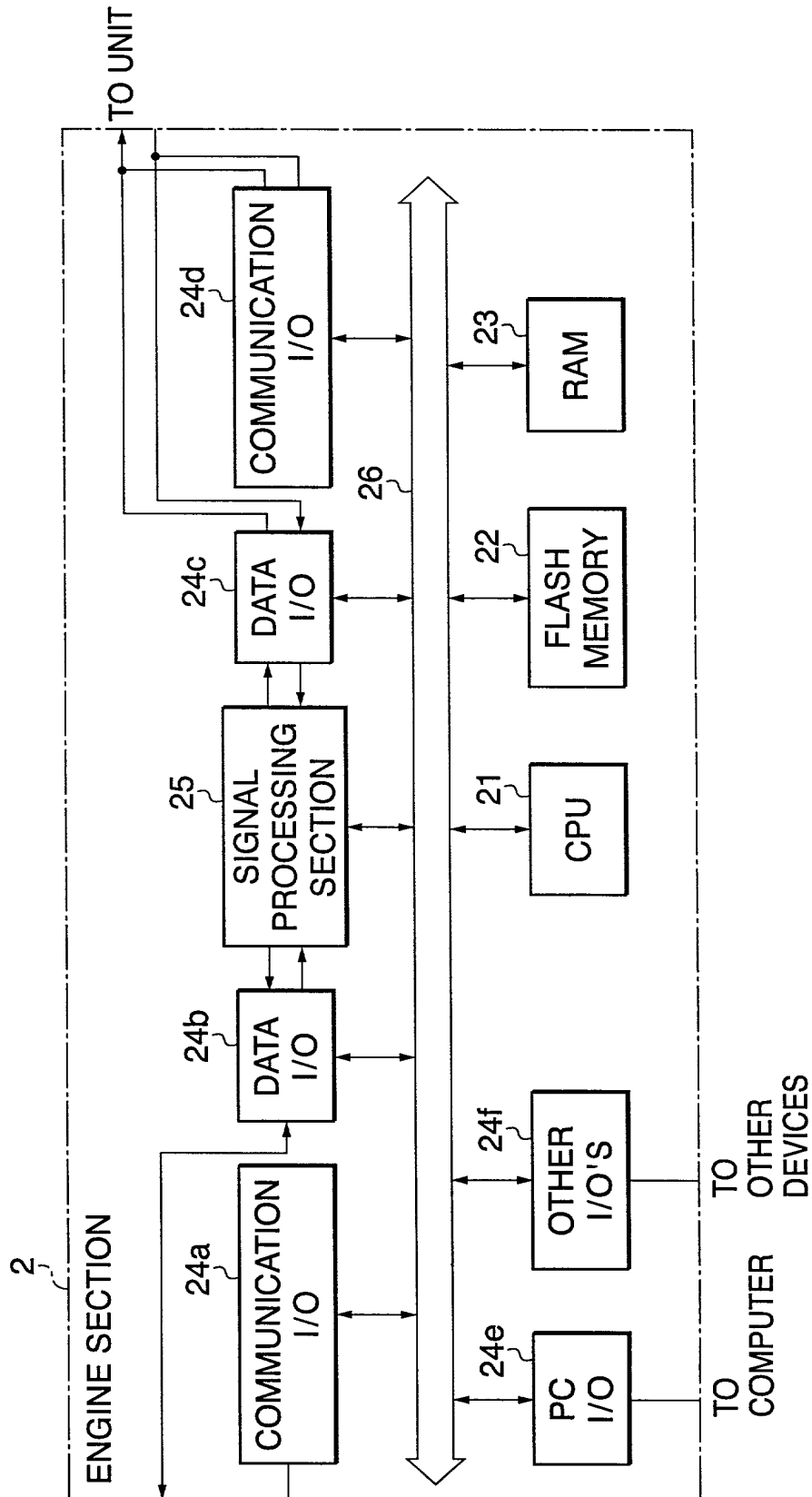
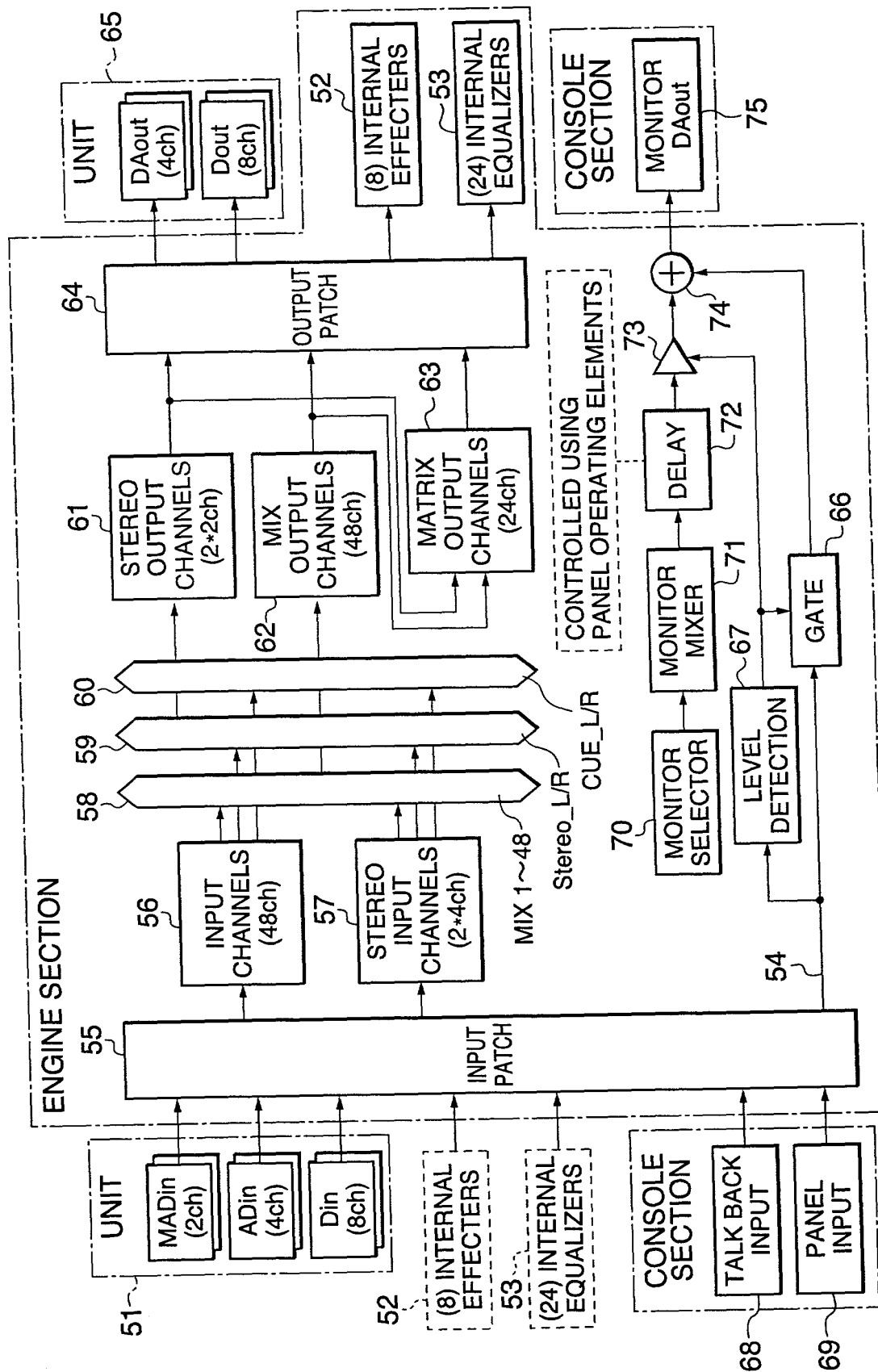


FIG. 4



**FIG. 5**



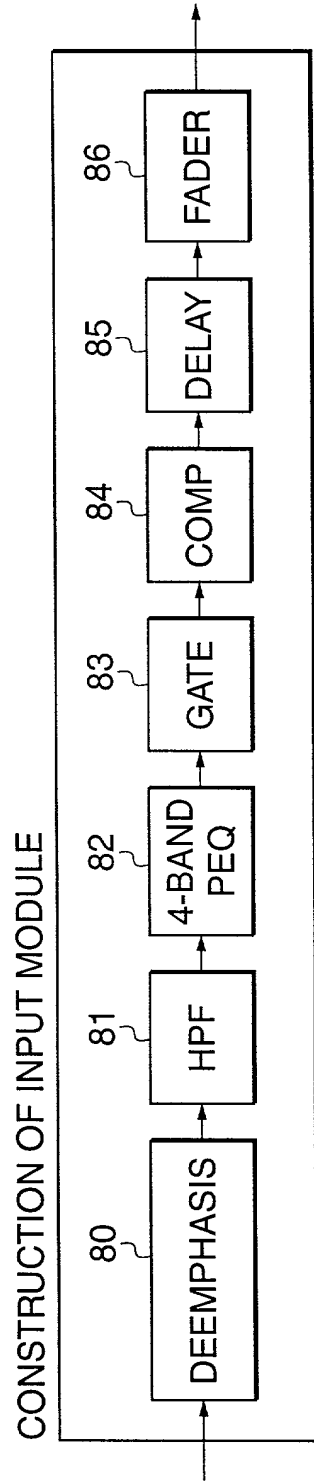


FIG. 6A

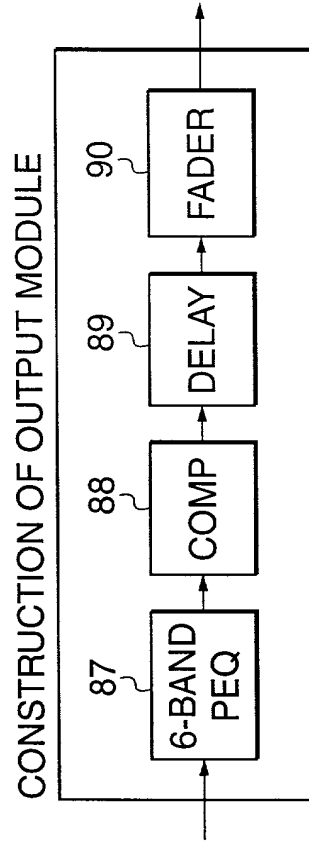


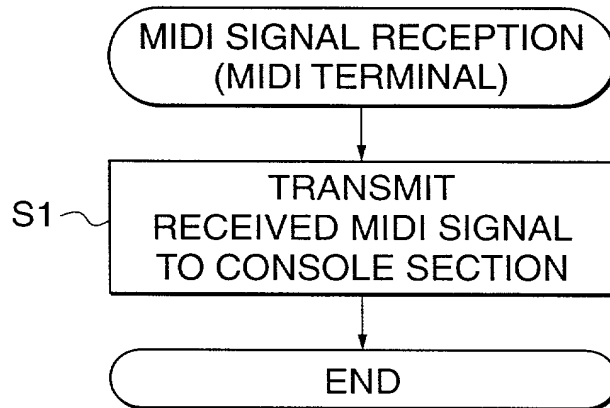
FIG. 6B

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**FIG. 7A**

{  
MODE = 0:INDEPENDENT OPERATION  
MODE = 1:CONSOLE ACTS AS MASTER  
MODE = 2:PC ACTS AS MASTER  
}

ENGINE SECTION (MODE=1)



**FIG. 7B**

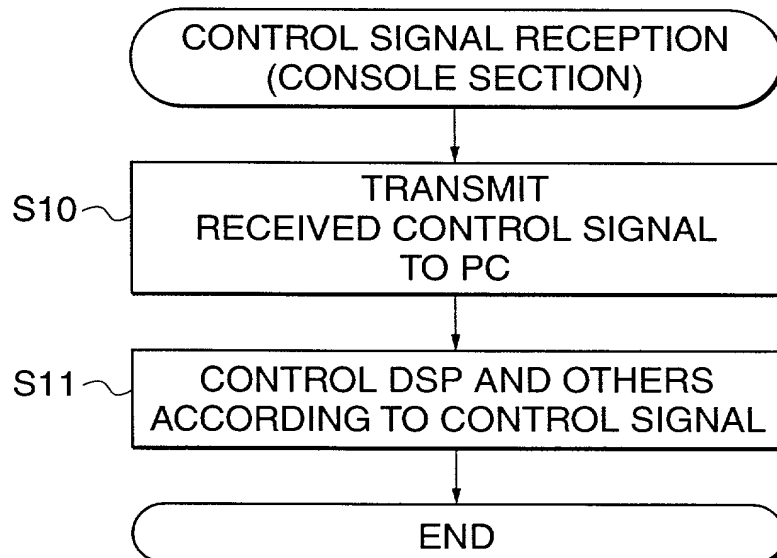
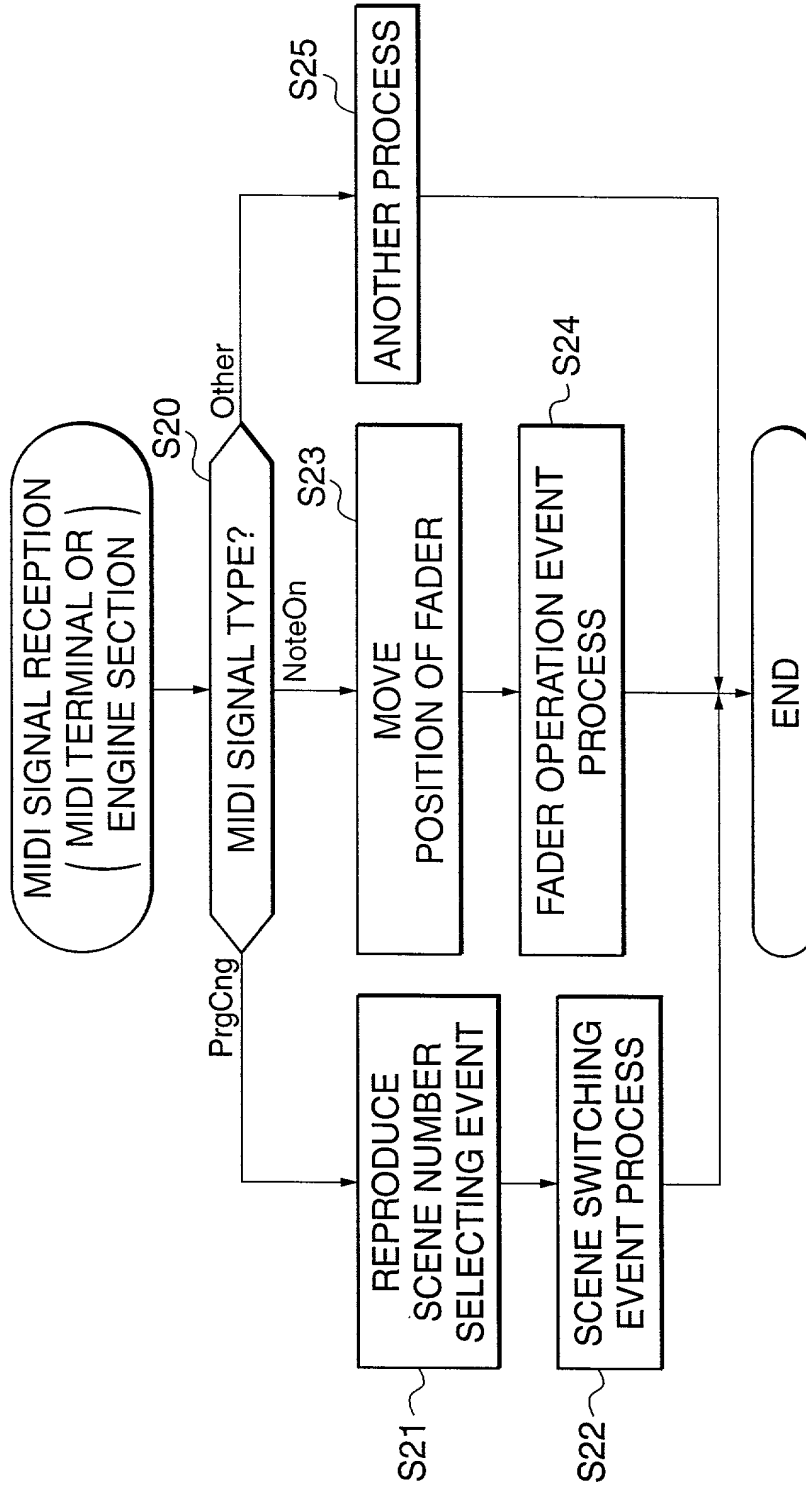


FIG. 8

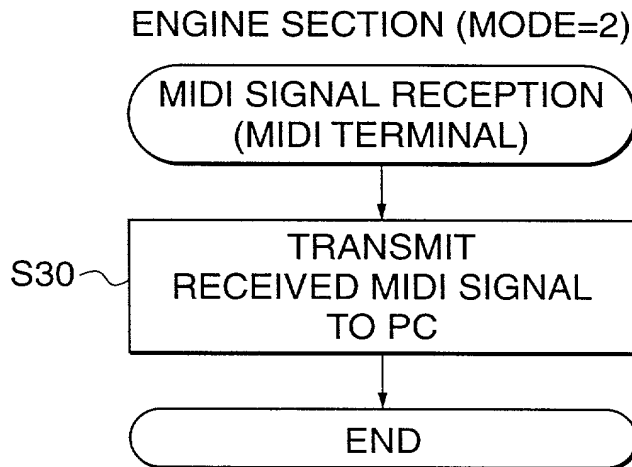
CONSOLE SECTION (MODE=1)





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**FIG. 9A**



**FIG. 9B**

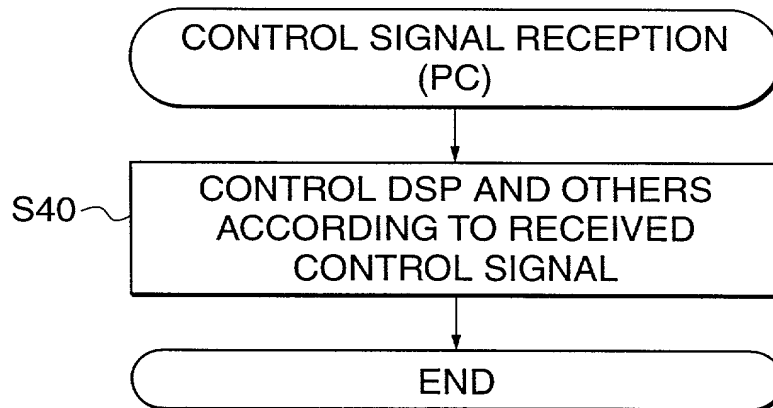
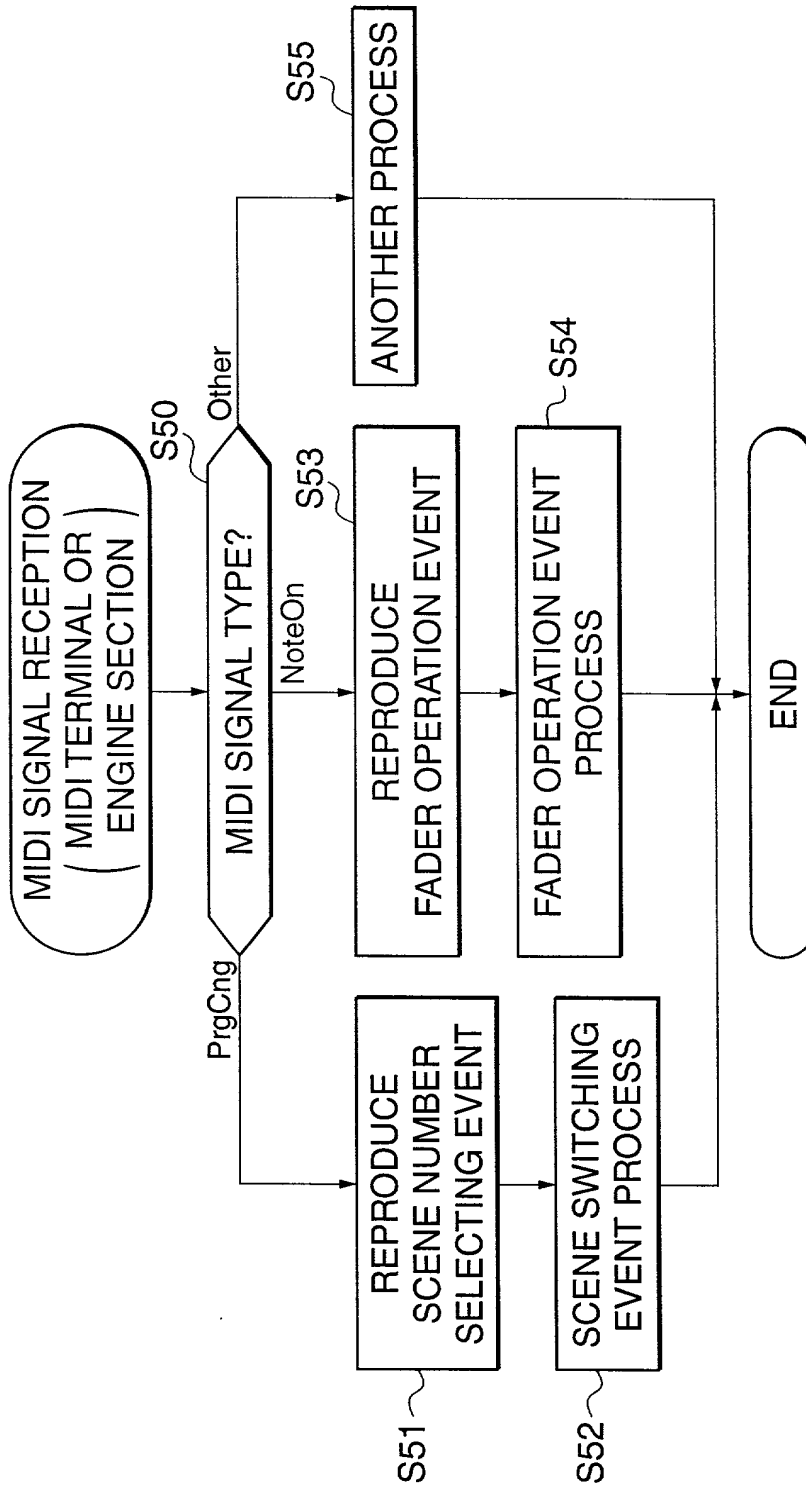


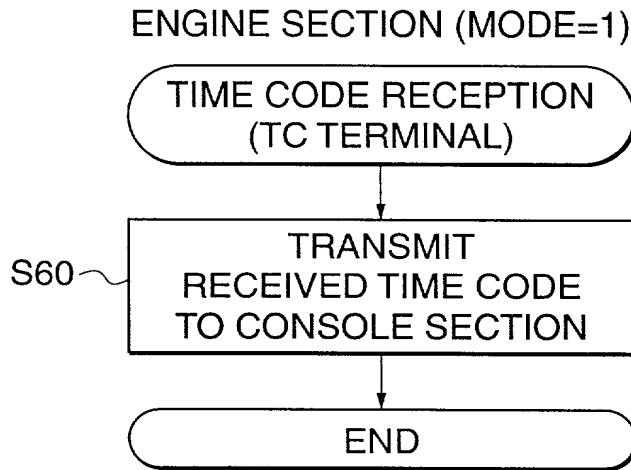
FIG. 10

PC (MODE=2)



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**FIG. 11A**



**FIG. 11B**

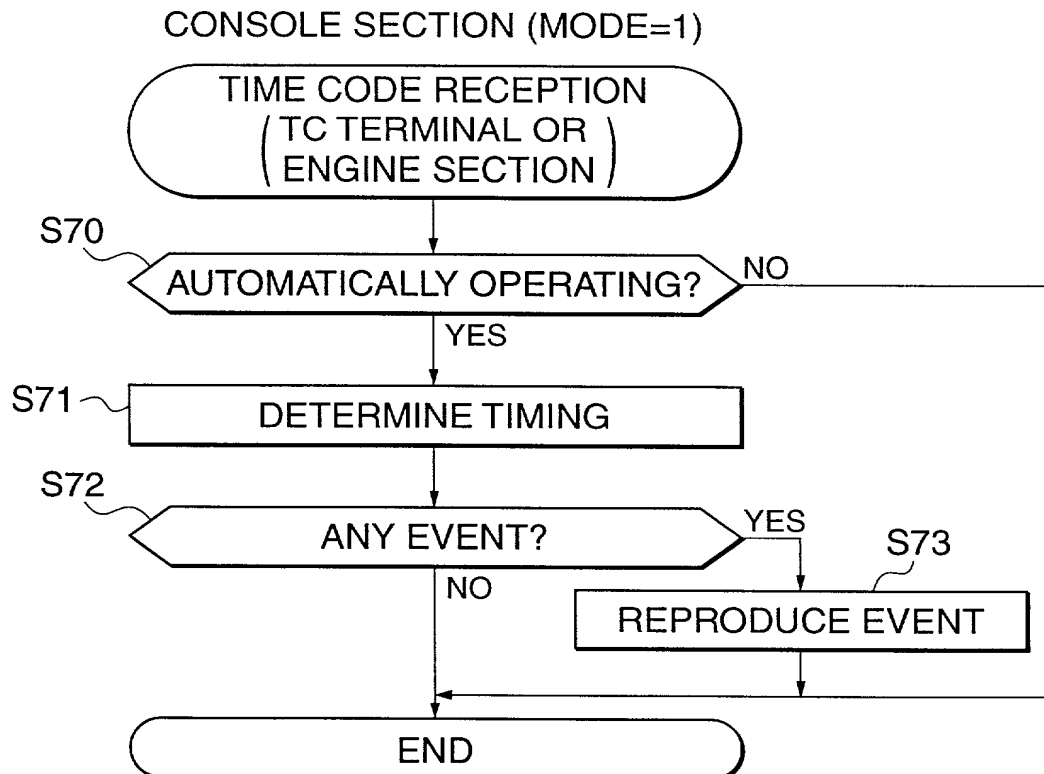


FIG. 12

ENGINE SECTION (MODE=1)

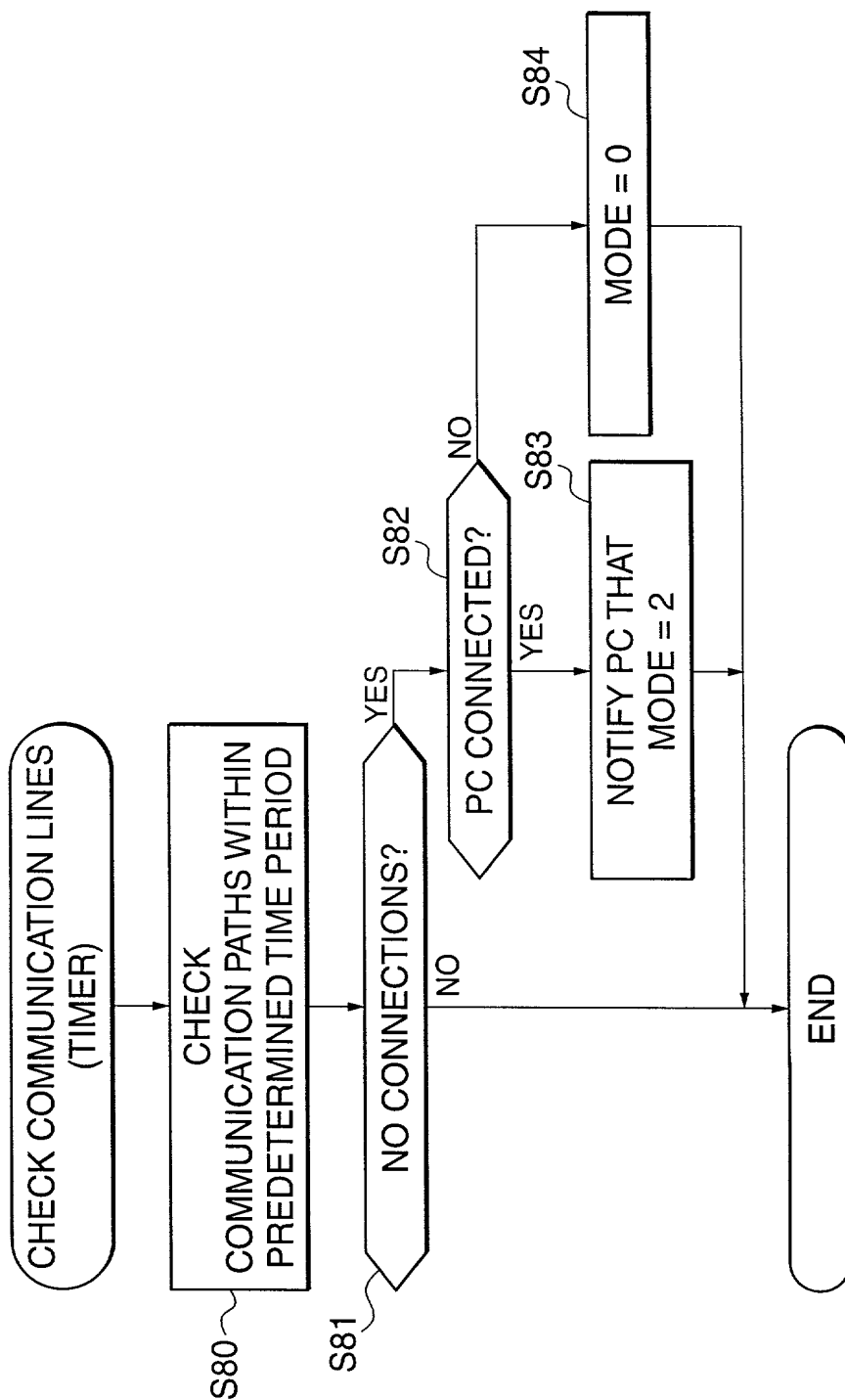
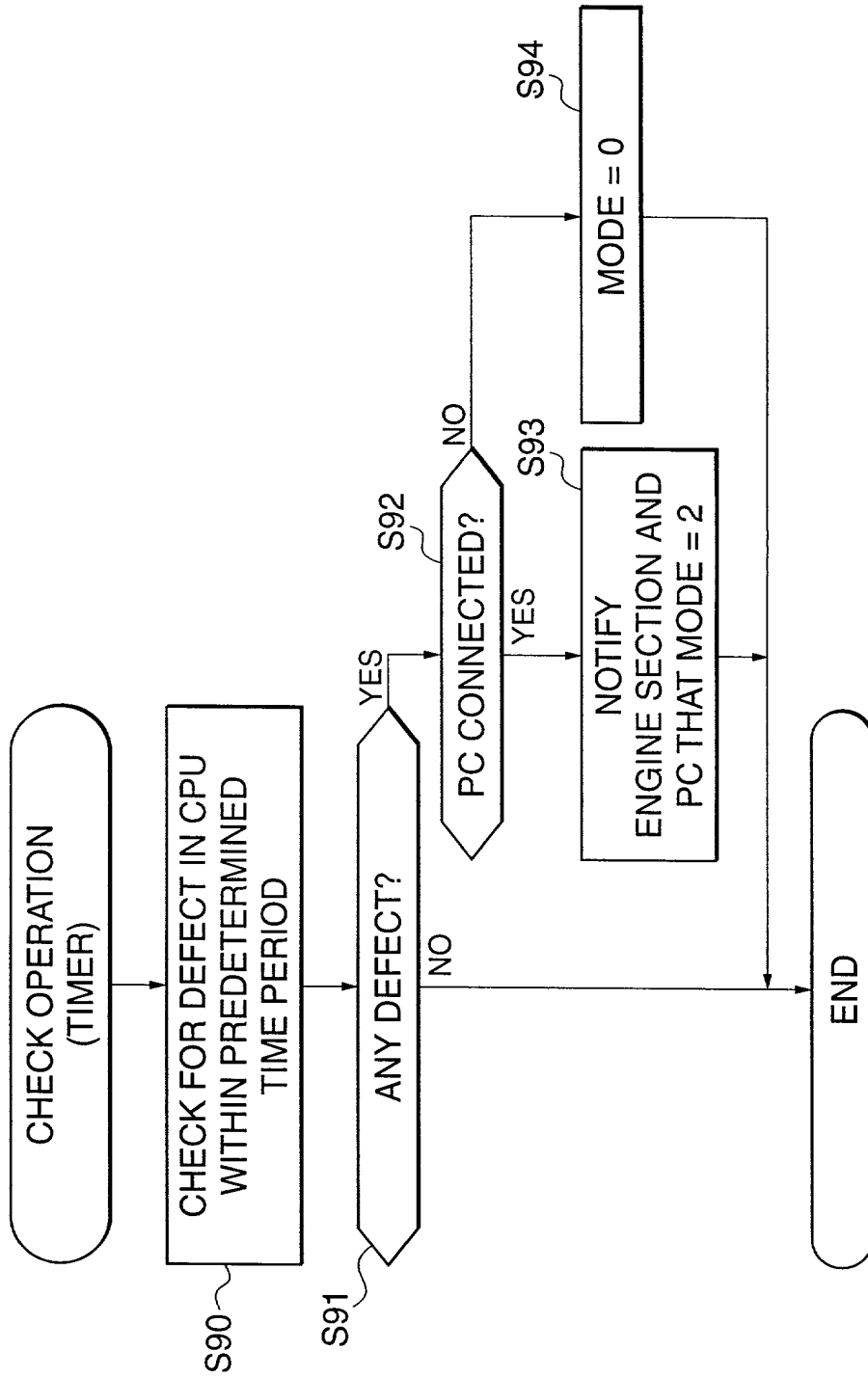
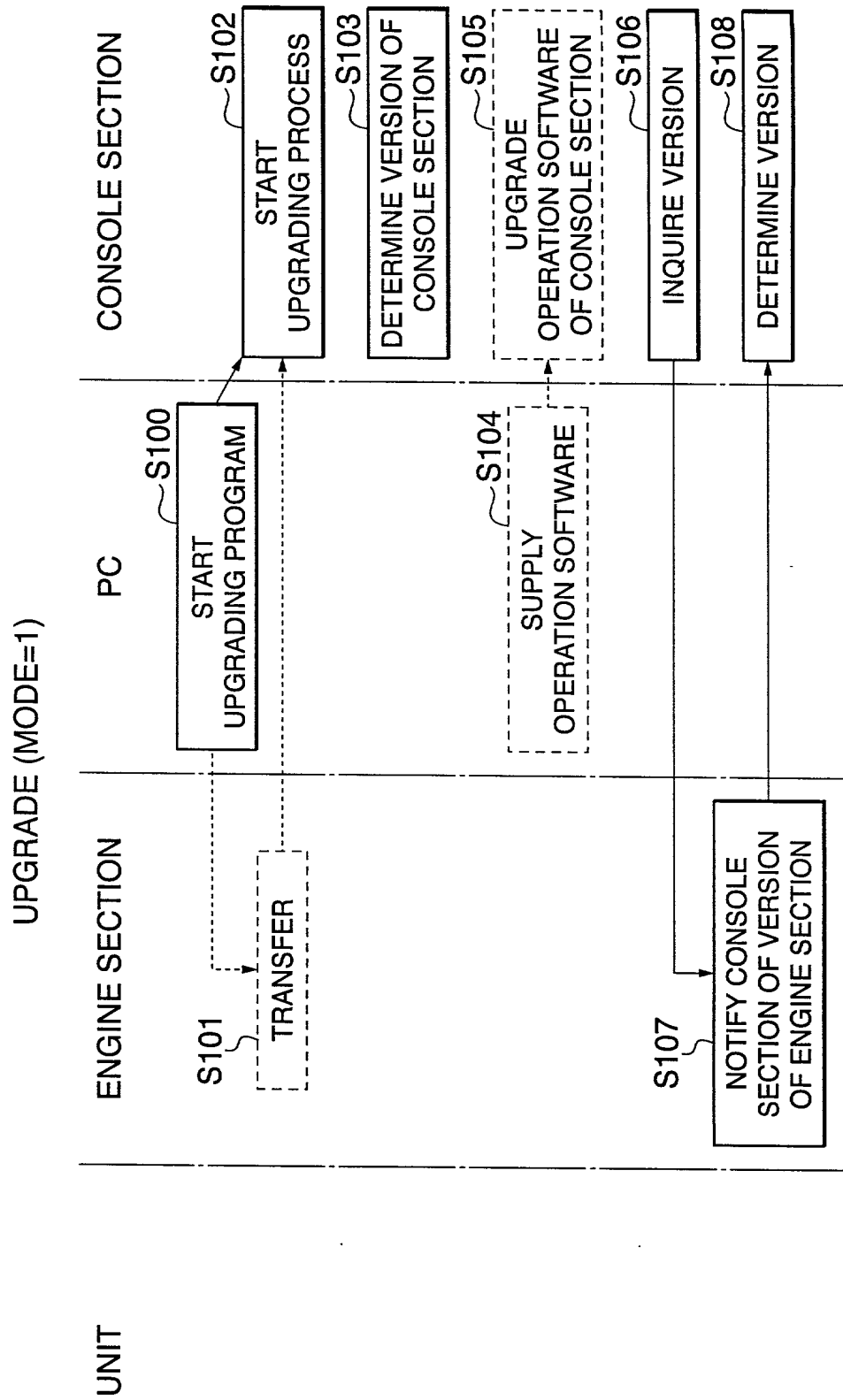


FIG. 13

CONSOLE SECTION (MODE=1)





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FIG. 14B

